

6/14/05

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

METHOD FOR CREATING A SELF-ALIGNED SOI DIODE BY
REMOVING A POLYSILICON GATE DURING PROCESSING

Application Number : 10/708912



Confirmation Number: 2911

First Named Applicant: Kiran Chatty

Attorney Docket Number: BUR920030180US1

Art Unit:

Examiner:

Search string: (5629544 or 5811857 or 6232163 or 6245600 or 6589823 or 6576958 or 6590264
or 20020119608 or 20030080386),pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|------|----------|------------|------------|--------------------|------|-------|----------|
| TS | 1 | 5629544 | 1997-05-13 | Voldman, et al | | | |
| | 2 | 5811857 | 1998-09-22 | Assaderaghi, et al | | | |
| | 3 | 6232163 | 2001-05-15 | Voldman, et al | | | |
| | 4 | 6245600 | 2001-06-12 | Geissler, et al | | | |
| | 5 | 6589823 | 2003-07-08 | Beebe, et al | | | |
| | 6 | 6576958 | 2003-06-10 | Ker, et al | | | |
| TS | 7 | 6590264 | 2003-07-08 | Ker, et al | | | |

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

| init | Cite.No. | Pub. No. | Date | Applicant | Kind | Class | Subclass |
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| TS | 1 | 20020119608 | 2002-08-29 | Ko, et al | | | |
| TS | 2 | 20030080386 | 2003-05-01 | Ker, et al | | | |

Signature

| Examiner Name | Date |
|---------------|----------|
| | 12/11/05 |